<u>**Amendments to the Claims:**</u>

This listing of claims will replace all prior versions and listings of claims in the Application:

<u>**Listing of Claims:**</u>

1. – 20. (cancelled)

21. (currently amended) A thin film transistor (TFT) array panel for a liquid crystal display, comprising:

a first line;

a second line intersecting the first line;

a pixel region defined by the intersection of the second line and the first line;

a pixel electrode formed in the pixel region; and,

a <u>ladder-shaped</u> conductive pattern connected to the second line and having a <u>pair of parallel, spaced-apart riser-like elements respectively disposed on opposite</u> ~~ladder-shape and at least two branches disposed at both~~ sides of the first line.

22. (previously presented) The TFT array panel of claim 21, further comprising a storage wire including a storage electrode line.

23. (previously presented) The TFT array panel of claim 21, further comprising a passivation layer formed between the pixel electrode and the first line and the second line.

24. (currently amended) A thin film transistor (TFT) array panel for a liquid crystal display, comprising:

a first line;

a common wire including a common electrode line;

a second line intersecting the first line;

a pixel region defined by the intersection of the first line and the second line;

a pixel electrode formed in the pixel region and overlapping the common electrode; and,

a <u>ladder-shaped</u> conductive pattern connected to the second line and having a <u>pair of parallel, spaced-apart riser-like elements respectively disposed on opposite</u> ~~ladder-shape and at least~~

~~two branches disposed at both~~ sides of the first line.

25. – 30. (cancelled)

31. (previously presented) The TFT array panel of claim 22, wherein the conductive pattern and the storage wire are formed in the same layer.